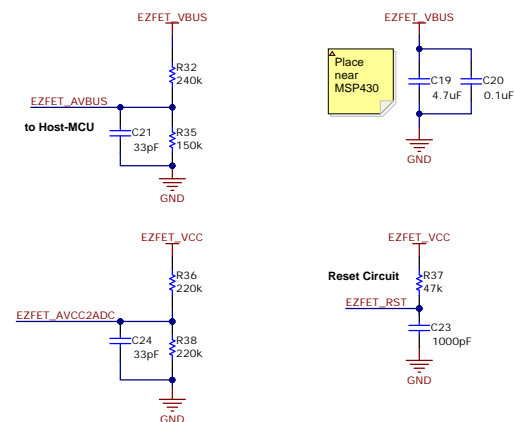
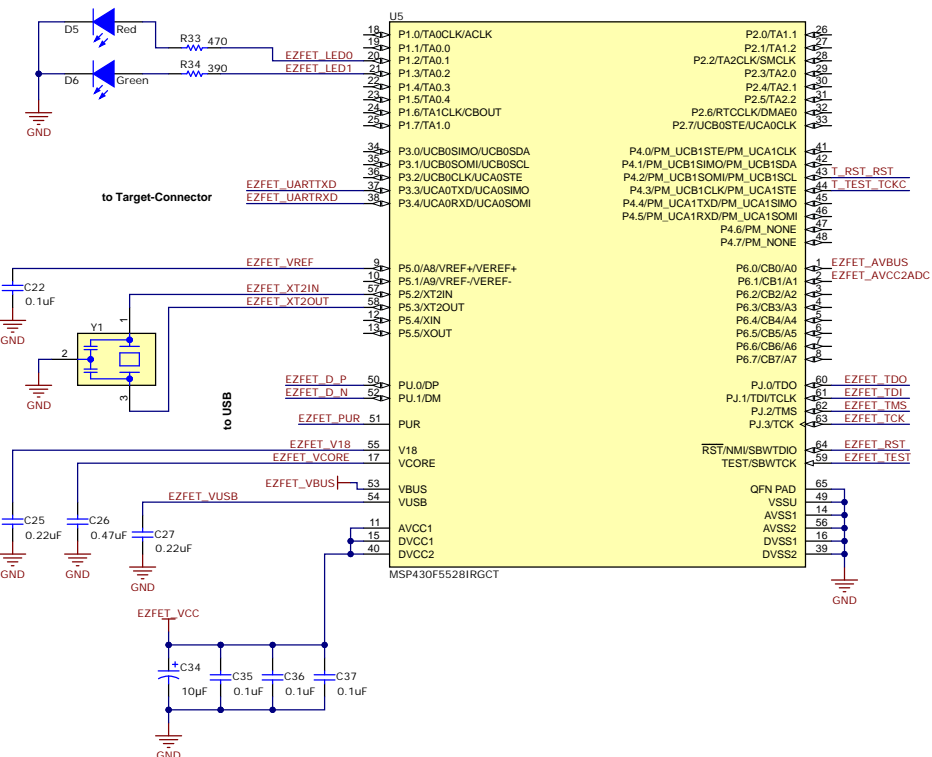
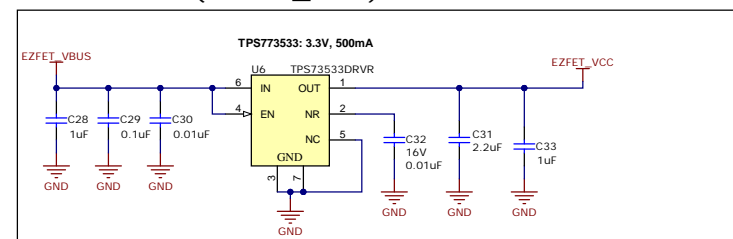


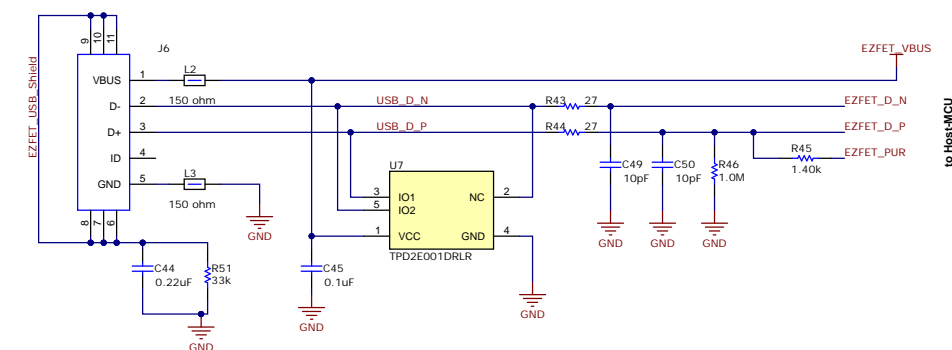
## Host MCU for Emulation



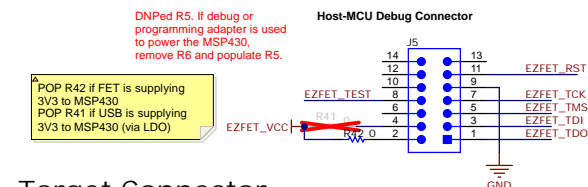
### 3.3V Power (EZFET\_VCC)



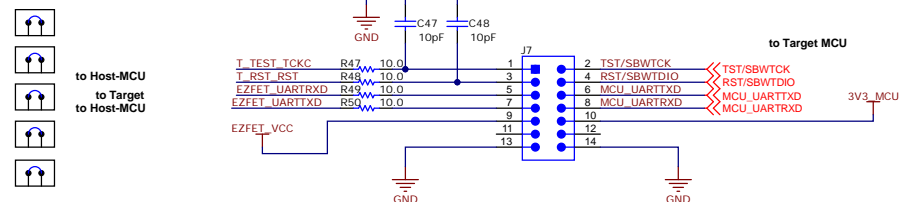
## USB-Interface



## JTAG-Connector (Host Debug)



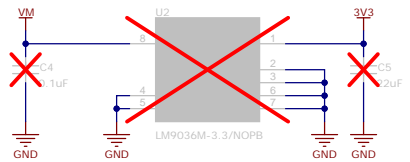
## Target Connector



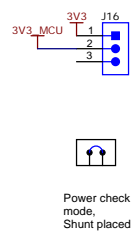
Orderable: <b>DRV8242H-Q1EVM</b>	Designed for: <b>Public Release</b>	Mod. Date: 7/3/2023	 <b>TEXAS</b> <b>INSTRUMENTS</b>
TID #: <b>N/A</b>	Project Title: <b>MD074</b>		
Model: <b>MD074</b>	Rev: <b>A</b>	Sheet Title:	
SVN Rev: <b>c31e316dc7db0a6978f56e7094b4d0100</b>		Sheet: <b>1</b> of <b>3</b>	
Drawn By: <b>Engineer: Shinya Morita</b>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	Size: <b>B</b>	
			<a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2023

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

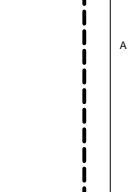
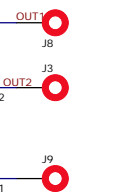
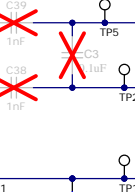
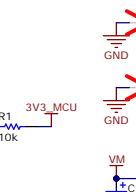
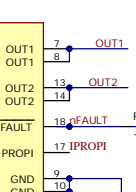
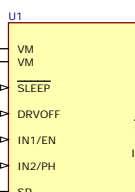
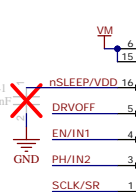
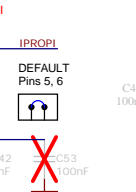
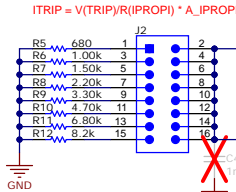
## 3.3V LDO



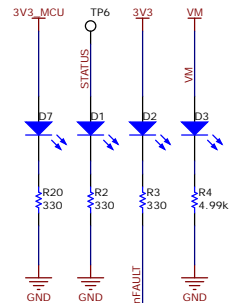
## Power Check



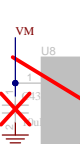
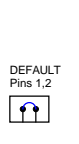
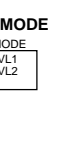
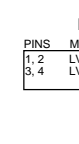
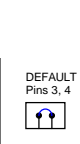
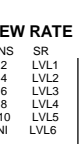
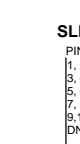
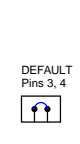
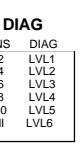
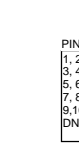
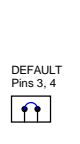
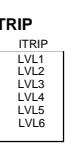
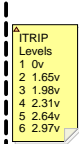
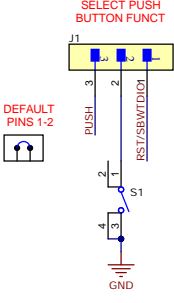
## DRV8242H/S/P-Q1 QFN



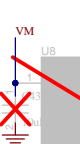
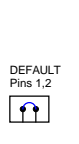
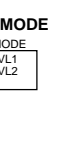
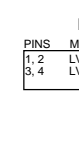
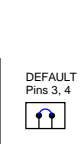
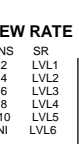
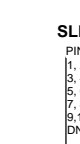
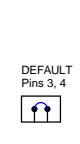
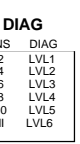
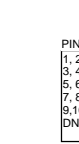
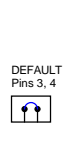
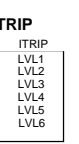
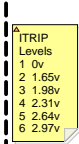
## LEDS



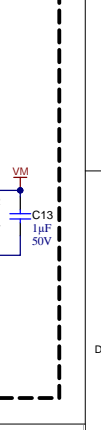
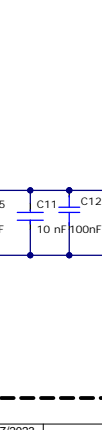
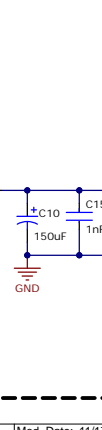
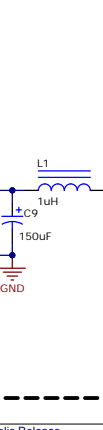
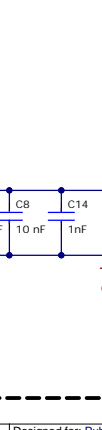
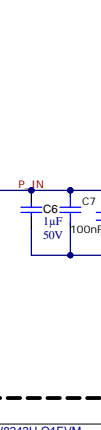
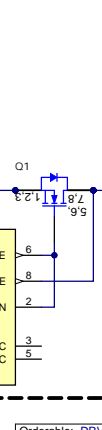
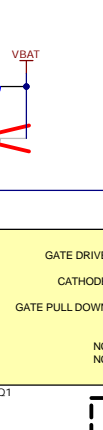
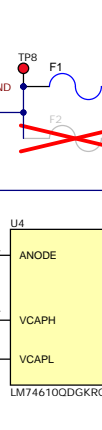
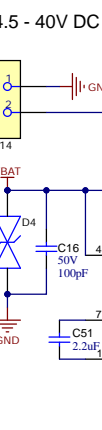
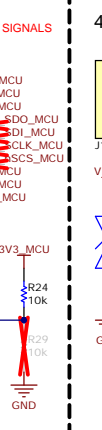
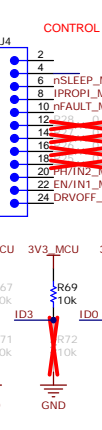
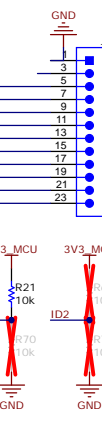
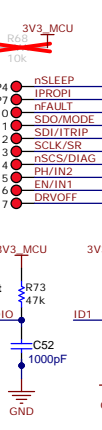
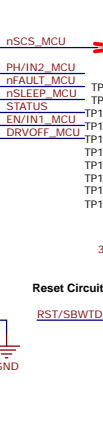
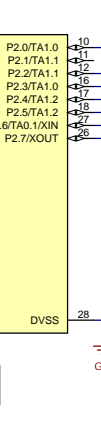
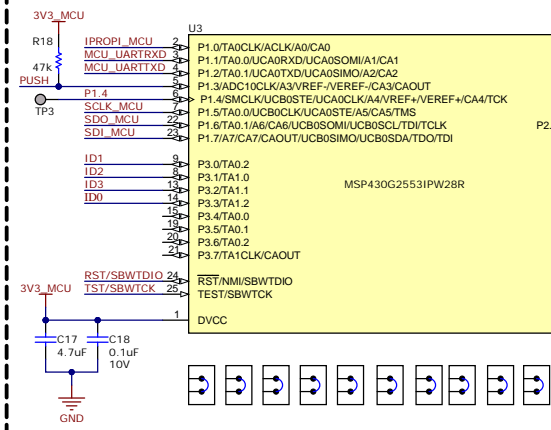
## BUTTON



## ANALOG CONTROL SIGNALS (H-variant only)

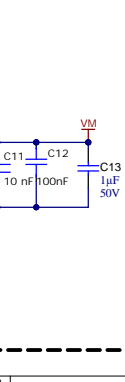
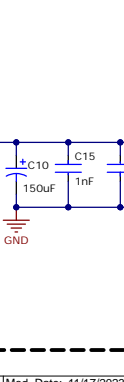
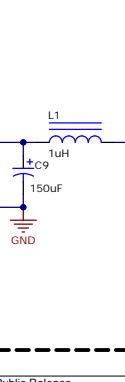
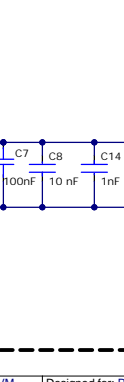
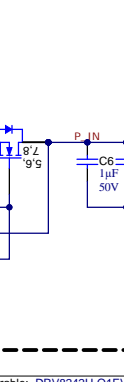
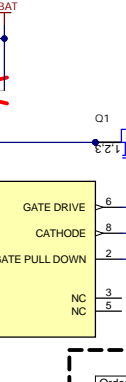
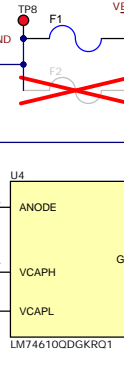
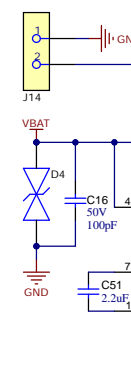


## MSP430



## MAIN INPUT SUPPLY REVERSE PROTECTION &amp; FILTER

## 4.5 - 40V DC



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

H1  
SJ-5303 (CLEAR)

H2  
SJ-5303 (CLEAR)

H3  
SJ-5303 (CLEAR)

H4  
SJ-5303 (CLEAR)

FID1

FID2

FID3

PCB Number: MD074  
PCB Rev: A

PCB  
LOGO  
Texas Instruments



PCB  
LOGO  
FCC disclaimer

PCB  
LOGO  
WEEE logo



CAUTION HOT SURFACE

PCB  
LOGO  
CAUTION. READ USER GUIDE BEFORE USE

Variant/Label Table

Variant	Label Text
001	DRV8242H-Q1EVM
002	DRV8242S-Q1EVM
003	DRV8242P-Q1EVM

LBL1

PCB Label

TH1-14-423-10  
Size: 0.65" x 0.20"

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: DRV8242H-Q1EVM	Designed for: Public Release	Mod. Date: 7/3/2023	
TID #: N/A	Project Title: MD074		
Number: MD074	Rev: A	Sheet Title: DRV8242H-Q1EVM	
SVN Rev: c31e316dcd7dba06a978d5e679b40d0a000	File: MD074A_Hardware.SchDoc	Sheet 3 of 3	
Drawn By: Shinya Morita	Contact: http://www.ti.com/support	Size: B	

http://www.ti.com  
© Texas Instruments 2023